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09/911752
07/23/01

ISSUE CLASSIFICATION	
Class	Subclass

PATENT NUMBER

U.S. UTILITY Patent Application

O.I.P.E. SCANNED <u>TD4</u> Q.A. <u>TM</u>	PATENT DATE
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APPLICATION NO. 09/911752	CONT/PRIOR	CLASS 855-361	SUBCLASS 785	ART UNIT 2818 2827	EXAMINER DINH
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TITLE	APPLICANTS
1. Chairman	1. Mr. J. Edgar Hoover
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James McCall
Hing To

Systems having modules with buffer chips

PTO-2040
12/99[illegible]

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